

ABSTRACT OF THE DISCLOSURE

In an offset control circuit, a voltage/current converting portion generates differential current (I^+ and I^-) that are proportional to a potential difference between differential input voltage signals (VIN^+ and VIN^-), and an offset adjusting current-generating portion generates offset adjusting currents ($Iofs^+$ and $Iofs^-$). In a current/voltage converting portion, a current (I_r) that is proportional to a potential difference between differential terminals flows through. Differential current output terminals, offset adjusting current-output terminals and the differential terminals are connected. The offset components contained in the differential input voltage signals (VIN^+ and VIN^-) are adjusted with the offset adjusting currents ($Iofs^+$ and $Iofs^-$), and differential output voltage signals (VO^+ and VO^-) in which the offset components are added to the differential input voltage signals (VIN^+ and VIN^-) are generated.